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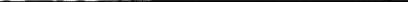
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<p>Substitute for form 1449A/PTO</p> <h1 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h1>				Complete If Known	
Sheet	2	of	2	Application Number	10/750,148
				Filing Date	December 31, 2003
				First Named Inventor	Blaise B. Fanning
				Art Unit	2186
				Examiner Name	Lev Iwashko
				Attorney Docket Number	42P16958

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
PB		KIM et al - IEEE Transactions on VLSI, Vol. 9, No. 4, pp. 509-523 - Aug 2001 - A reconfigurable multi-function computing cache architecture	
PB		ZHANG et al - ACM Transactions on Embedded Computing Systems, Vol. 4, No. 2, May 2005, Pages 363-387 - A highly configurable cache for low energy embedded systems	

Examiner Signature		Date Considered	11/14/06
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